

Docket No.: 57454-334

#6  
**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Satoshi KUMAKI, et al.

Serial No.: 10/053,545

Filed: January 24, 2002



: Customer Number: 20277

: Confirmation Number: 3322

: Group Art Unit: 2187

: Examiner: Ngoc V. Dinh

For: DATA PROCESSOR AND DATA PROCESSING METHOD REDUCED IN POWER  
CONSUMPTION DURING MEMORY ACCESS

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Mail Stop Information Disclosure Statement  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

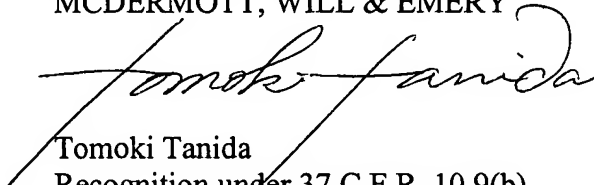
Supplemental to the Information Disclosure Statement filed January 24, 2002, and in particular the reference "Mitsubishi Semiconductor Data Book 1997 Memory SRAM" cited in the form PTO-1449, this reference simply provides general information of an SRAM. Please note that page 2-10 of the reference shows a block diagram of an SRAM, together with Applicants' annotations with respect to elements therein.

It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



Tomoki Tanida  
Recognition under 37 C.F.R. 10.9(b)

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
(202) 756-8000 SAB:TT:lnm  
Facsimile: (202) 756-8087  
**Date: March 17, 2004**